

# Enhanced Field Emission of Carbon Nanotubes Based Cold Cathode by Silicon Dioxide Buffer Layer

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Abstract: Electron emission is defined as the liberation of free electrons from a surface of the substance into vacuum, caused by external energy transferred to the electron. The emission of electrons from a metal or semiconductor surface under the influence of a strong electric field is called as field emission. Classically, electrons cannot cross the potential barrier if it has less energy than potential barrier but Quantum mechanically electrons has some probability to cross the potential barrier. In our work, a buffer layer of Silicon Dioxide is deposited prior to the catalyst deposition and the Carbon Nanotubes growth was done by thermal chemical vapor deposition method. There was significant improvement in various field emission parameters such as emission current density (J) increases from 1.8 mA/cm<sup>2</sup> to 22.53 mA/cm<sup>2</sup>, turn on field (E<sub>T</sub>) decreases from 3.4 V/µm to 1.5 V/µm, field enhancement factor ( $\beta$ ) increases from 1229 to 9325 and effective emitting area ( $\alpha$ ) increases from 7.54\*10<sup>-9</sup> cm<sup>-2</sup> to 1.45\*10<sup>-11</sup> cm<sup>-2</sup> by the use of Silicon Dioxide buffer layer. The enhanced field emission properties suggest that the adhesion between Carbon Nanotubes and the substrate increased significantly. The higher current density signifies that the Silicon Dioxide buffer layer lowered the contact resistance and thus longer duration stability was achieved.

Keywords: Carbon Nanotube, Field Emission, Cold Cathode, Chemical Vapor Deposition, Buffer Layer

#### **INTRODUCTION**

Carbon Nanotubes (CNT's) [1] have excellent physical, mechanical, electrical properties and their potential applications in many peculiar areas like field emitter, nanosensor, fuel storage, nanoelectronics. Previous research investigations have shown that Carbon Nanotubes have outstanding field emission properties [2] due to its very high aspect ratio and very small and sharp tip radius. By virtue of this, Carbon Nanotubes based cold cathode are very efficient electron sources for various applications, including field-emission displays [3], x-ray tubes [4] and electron microscopy [5]. However, the current density obtained from present Carbon Nanotubes based cold cathode is still low. The low current density of CNT based cold cathode emitter is due to many factors, the crucial ones being degradation of CNTs at high electric field [6] and screening effect [7]. It has been predicted that electrostatic screening effect becomes minimum when height of the CNTs is about one half of the inter-nanotube distance [2]. Various schemes have been reported for the minimization of screening effect by growing CNTs bundles with different dot structure and sizes [8-9] rather than growing continuous films of CNTs. Different methods have been reported in the literature for controlling the growth density of CNTs, such as by the capping catalyst particles by deposition of Ti layer [10] and by deposition of buffer layer to enhance the adhesion of CNTs with substrate [11].

In this experiment, we use Silicon Dioxide  $(SiO_2)$  buffer layer for minimization of screening effect and improvement of adhesion of substrate and emitters results in increasing the

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current density. We have carried out our study on two types of samples of CNTs based cold cathode; the first sample without buffer layer referred as reference sample while the second sample having SiO<sub>2</sub> as buffer layer. To evaluate the SiO<sub>2</sub> buffer layer effect on screening, field emission measurement of the above cathodes were carried out in diode mode configuration. Our results validate that the deposition of SiO<sub>2</sub> buffer layer is a major aspect in the enhancement of the field emission parameters of CNT based field emitters.

### **METHODS**

N-type silicon (Si) wafer of low resistivity was taken as a base material or substrate. A thin film of thickness 2 nm of iron (Fe) as catalyst was developed on Si substrate by RF sputtering followed by lift-off. This sample is termed as reference sample. Another sample having SiO<sub>2</sub> buffer layer grown by RF sputtering prior to Fe as catalyst Samples used for the study is 10µm dot with 10 µm spacing. The Growth of CNTs on said samples was carried out by Thermal Chemical Vapor Deposition (CVD) at atmospheric pressure. Initially these patterned samples were heated to 850°C in presence of H<sub>2</sub>. Thereafter the samples were pretreated in the presence of NH<sub>3</sub> followed by the introduction of C<sub>2</sub>H<sub>2</sub> as the carbon feedstock to carry out the growth of CNTs on both the samples. Then characterizations of these samples were done using Scanning Electron Microscopy.

Field emission studies on reference sample without buffer layer and sample having  $SiO_2$  as buffer layer were carried out in diode configuration. In this arrangement CNTs were used as cathode and mounted on the stainless steel assembly. A stainless steel disk mounted on the same fixture acted as the anode. A screw at the top of the assembly allows us to control the electrode spacing very accurately. In our case the typical electrode spacing was kept at 500 microns. The entire assembly was kept

inside a vacuum chamber and evacuated to a vacuum better than 10<sup>-7</sup> Torr. The emission current measurement was carried out as a function of electric field, under very high vacuum conditions for both the samples of CNTs based cold cathode on silicon substrate.

### **RESULTS AND DISCUSSION**

SEM images of representative samples with and without buffer layer based cathodes prepared by Chemical Vapor Deposition technique are shown in Figure 1 and 2. Desired growth of CNTs has been obtained. The CNTs are found to be vertically aligned via self-supporting mechanism. Due to very high density, the heights of all the CNT samples are similar and are clearly visible from SEM images.



Figure 1-SEM image of reference sample



Figure 2-SEM image of sample with  $SiO_2$  buffer layer The JE plots of the samples are shown in Figure 3 and 4 respectively. It is quite clear from the JE plots that both the samples show the diode characteristics. It is also observed from the

JE plots that the lowest current density viz 1.8 mA/cm<sup>2</sup> was obtained in case of reference sample without any buffer layer shown in Figure 3, and in case of sample with SiO<sub>2</sub> buffer layer the current density obtained is 22.53mA/cm<sup>2</sup> shown in Figure 4. Considering samples, henceforth we observed that the current density for the sample with SiO<sub>2</sub> as buffer layer is more as compared to the reference sample. Though the patterning of dots per unit area in each of samples is same, the current increases in structure having buffer layer. It is clear from Figure 3 and 4, the turn on field decreases from 3.2 V/µm to 1.5 V/µm by using SiO<sub>2</sub> as buffer layer, which clearly confirms our argument that buffer layer is a very effective way to enhance the current density and turn on field.





Figure 4-JE plot of sample with SiO<sub>2</sub> buffer layer

The corresponding Fowler-Nordheim plots for both the samples are shown in Figure 5 and 6.

The two regions i.e. higher field region and lower field region in the F-N curve for both the samples are clearly visible. However it must be noted that in case of the buffer layered sample the change in slope in two regimes is very small while for reference sample the appearance of the knee is obvious. This can be ascribe to the fact that in case of buffer layer structure there is an exponential and stable increase of current even at high fields. However for the structures without buffer layer, it is clear from the JE plot the increase in current density shows some deviation from expected exponential behavior. This behavior might be due to the elimination of emitting tips at high fields which result in the decrease of tip density during emission. However for the structures with buffer layer, the decrease shielding due to tip elimination at high field does not play a dominant role.



Figure 6-FN plot of sample with SiO<sub>2</sub> buffer layer

In our work, There was significant improvement in other field emission parameters such as field enhancement factor ( $\beta$ ) increases from 1229 to 9325 and effective emitting area ( $\alpha$ ) increases from 7.54\*10<sup>-9</sup> cm<sup>-2</sup> to 1.45\*10<sup>-11</sup> cm<sup>-2</sup> by the use of SiO<sub>2</sub> buffer layer. The emissions from cathodes show a very smooth curve which are almost close to the ideal JE behavior. Results for our cathodes were repeatable for several cycles and also achieved enhanced field emission parameters at comparatively lower electric fields.

### Conclusion

In this work we used CVD technique to deposit CNTs film on Silicon (Si) substrates with and without intermediate buffer layer. The first sample without buffer layer is called as reference sample and another with SiO<sub>2</sub> buffer layer. In our work, There was significant improvement in various field emission parameters such as emission current density (J), Turnon field ( $E_T$ ), field enhancement factor ( $\beta$ ) and effective emitting area ( $\alpha$ ) increases by the use of SiO<sub>2</sub> buffer layer. The enhanced field emission properties suggest that the adhesion between CNTs and the substrate increased significantly. The higher current density signifies that the SiO<sub>2</sub> buffer layer lowered the contact resistance and thus longer duration stability. These facts are useful and might shed new light on the field emission mechanism for CNTs.

### ABBREVIATIONS

CVD- Chemical Vapor Deposition, CNT- Carbon Nanotube, FN- Fowler-Nordheim, SEM- Scanning Electron Microscope, RF- Radio Frequency,

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